



2826

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Fred Fishburn

Title: INTEGRATED CIRCUIT HAVING A BARRIER STRUCTURE

Docket No.: 303.703US1

Serial No.: 09/653,640

Filed: August 31, 2000

Due Date: March 10, 2003

Examiner: Alexander Williams

Group Art Unit: 2826

Commissioner for Patents
Washington, D.C. 20231

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We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ An Amendment and Response (13 Pages).
- ☒ 14 sheets of Formal Drawings.

Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900)

By: Daniel J. Kluth
Atty. Daniel J. Kluth
Reg. No. 32,146

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 10 day of March, 2003.

Name

Tina Kohn

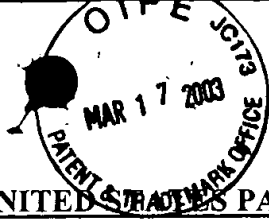
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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
(GENERAL)

P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900)



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S/N 09/653640

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Fred Fishburn

Examiner: Alexander Williams

Serial No.: 09/653640

Group Art Unit: 2826

Filed: August 31, 2000

Docket No.: 303.703US1

Title: CONTACT STRUCTURE

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Commissioner for Patents
Washington, D.C. 20231

Applicant has reviewed the Office Action mailed on December 10, 2002. Please amend the above-identified patent application as follows.

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IN THE DRAWINGS

Corrected formal drawings are supplied herewith. Accordingly, new FIG. 1A is being added herewith. FIG1A is a replica of originally submitted FIG. 1 with reference numeral 105 now being labeled 105A, and appropriate reference thereto made in the specification, with the amendments to the specification made herein below.

Support for the amended specification is found on page 4, lines 18-21, where the specification recites "[d]evices 103 and 105 may be any type of active or passive device capable of being fabricated using integrated circuit technologies, such as metal-oxide semiconductor (MOS) or bipolar technologies." No new matter has been introduced.

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